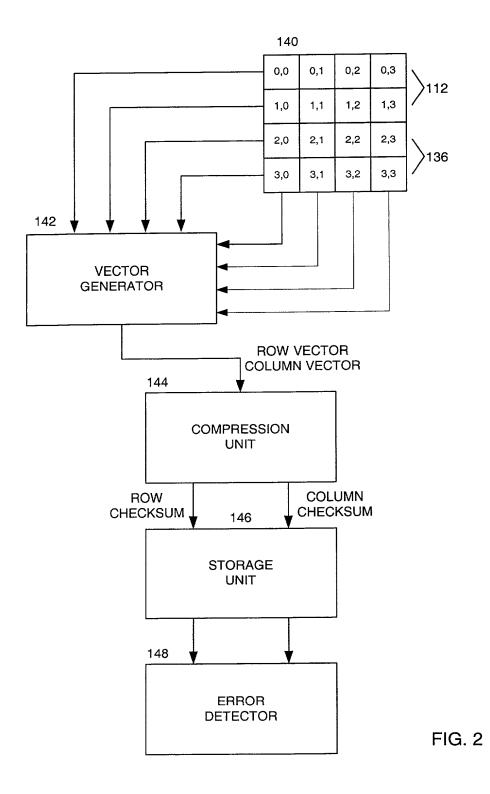


FIG. 1



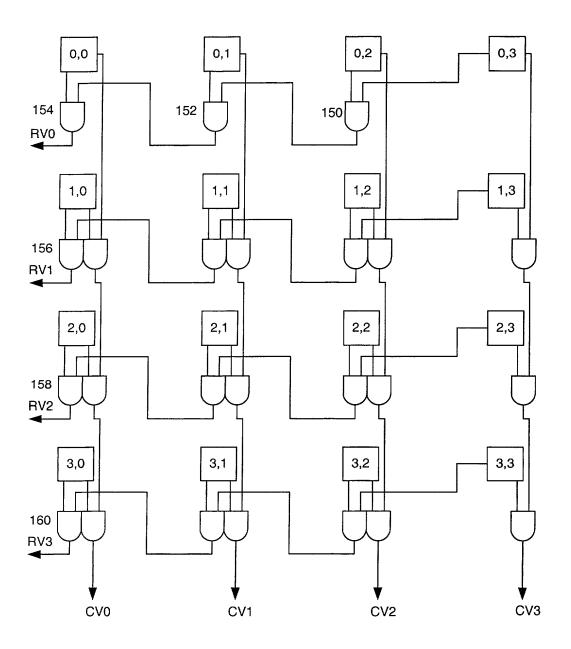


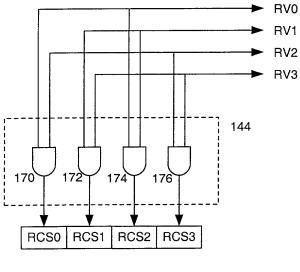
FIG. 3

ROW VECTOR	ADDRESS		BIT SELECTION			
	Az	Aı	A1=0	A1 = 1	A2=0	A2=1
Rvo	0	. 0	×		x	
Rv1	0	١		×	×	
RVZ	1	0	x			×
RV3	1	١		×		×
	,	ı	1		'	
			RCSO	RCS1	RC52	RCS3

F19.4

COLUMN	ADDRESS		BIT SELECTION			
VECTOR	Az	Δı	A1 = 0	A,= 1	A2=0	A,= (
CYO	0	0	×		×	$\overline{\bigcap}$
CV1	0	1		×	×	
CVZ	1	D	×			x
CV3	1	1		\times		X
ı				,		
			CCSO	CCS1	CCS 2	CC\$3

Fig. 5



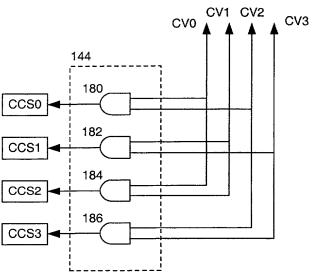


FIG. 6